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Attorney for Applicant(s)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.

10/047,769

[87,980]

Applicant

Daniel L. Klave et al.

Title

SYSTEMS AND METHODS FOR REDUCING MEMORY

USAGE IN AN EMBEDDED SYSTEM BY LOADING

INDIVIDUAL SOFTWARE COMPONENTS

Filed

January 15, 2002

RECEIVED

Confirmation No.

9701

TC/A.U.

2122

Examiner

Kakali Chaki

Technology Center 2100

MAR 1 7 2004

Docket No.

SLA1062

Customer No.

21552

TRANSMITTAL OF SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 TO PROPERTY OF THE PROPERTY OF

Dear Sir:

Transmitted herewith is a Supplemental Information Disclosure Statement disclosing information which has come to the attention of applicants and/or their attorneys and is being submitted so as to comply with the duty of disclosure set forth in 37 C.F.R. § 1.56. In accordance with 37 C.F.R. § 1.97(b), the enclosed Statement is being filed within three (3) months of the filing date of the above-identified application or before the mailing date of a first Action on the merits.

Neither applicants nor their attorneys make any representation that any information disclosed herein may be "prior art" within the meaning of that term under 35 U.S.C. § 102 or § 103. Moreover, pursuant to 37 C.F.R. § 1.97, the filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made or as an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

In accordance with 37 C.F.R. § 1.98, transmitted herewith are:

- A completed copy of Form PTO/SB/08A "Information Disclosure Statement by Applicant" listing the patents, publications and other information being submitted for consideration; and
- 2. A legible copy of each patent, publication and other item of information in written form listed on the enclosed Form PTO/SB/08A, except for copies of U.S. patents and published U.S. patent applications which are not required for applications filed after June 30, 2003.

NON-ENGLISH INFORMATION

Pursuant to 37 C.F.R. § 1.98, following is a concise explanation of the relevance (as it is presently understood by the individual designated in 37 C.F.R. § 1.56(c) most knowledgeable about the content of the information), of each listed patent, publication or other information that is not in the English language:

1. Japanese Patent Application No. JP2000-082010 published March 21, 2000 discloses a method to provide a data processor which enables fast access to data by transferring data stored in a slow-access memory to a fast-access memory more efficiently. The processor has adders 704 and 705 which add an address signal and address conversion data set by a

central processor and obtain a converted address signal corresponding to the address signal and a selector 713 which selects the converted address signal obtained by the address 704 and 709 or the address signal according to detection signals outputted from coincidence detecting circuits 708 and 709 deciding whether or not the address signal matches the ROM address of the conversion source and the processes of the adders 704 and 705 and coincidence detecting circuits 708 and 709 are performed in parallel.

Respectfully submitted,

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MAR. 1 5. 2004 85

PTO/SB/08a (08-03) Approved for use through 06/30/2006. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 Of

Complete if Known				
Application Number	10/047,769			
Filing Date	January 15, 2002			
First Named Inventor	Daniel L. Klave			
Group Art Unit	2122			
Examiner Name				
Attorney Docket Number	SLA1062			

U.S. PATENT DOCUMENTS					
Examiner	Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
Initials *		Number - Kind Code ^{2 (# known)}			
	U1	US-6,397,317	05/28/2002	Kusutaki	

	FOREIGN PATENT DOCUMENTS					
Examiner	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁸
Initials*		Country Code ³ - Number ⁴ - Kind Code ⁵ (#known)	MM-DD-YYYY			
	F1	JP - 2000-082010	03/21/2000	Kusutaki		

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	Examiner Signature	 Date Considered		
1	Signature	Considered	1	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you are required to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.